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Chapter 3 focuses on the synthesizable Verilog constructs, with examples on reusable design (parameterized design, functions, and generate structure). Chapter 5 describes the basic concepts in digital design - logic gates, truth table, De Morgan's theorem, set-up and hold time, edge detection, and number system.

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It is an advanced digital logic design textbook that emphasizes the use of synthesizable Verilog code and provides numerous fully worked-out practical design examples including a Universal Serial Bus interface, a pipelined multiply-accumulate unit, and a pipelined microprocessor for the ARM THUMB architecture.

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"This is a valuable addition to the rather scant practical guides available today for digital designers. What is impressive about this book is the range covered - 700 pages of insights in various aspects of digital design from basic Verilog to complex issues like DMA, arbitration, clock design including practical guide to optimizations for throughput, power, and performance.

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